

Agenda for the 2024Q2 CMC Meeting Micron Technology, Inc., San Jose, California

Thursday, June 13, 2024

Version 2

MEMBERS AND GUEST COMPANIES ONLY

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|---------|---|--|
| 9:00 AM | Agenda Review and Introductions (10 min.) | Peter Lee (Micron) |
| 9:10 AM | Update from Si2 (30 min.) | Leigh Anne Clevenger (Si2) for Robert Aslett (Si2) |
| 9:40 AM | Treasurer's Report (5 min.) | Peter Lee (Micron) for Takeshi Naito (KIOXIA) |
| 9:45 AM | CMC Operational Items (5 min.) | Peter Lee (Micron) |
| 9:50 AM | Break (10 min.) | |

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ALL (includes Model Developers)

10:00 AM	EDA Vendor Reports (30 min.)	EDA Vendors
10:30 AM	Mextram (15 min.)	Wei Chen (TI)
10:45 AM	HICUM_L0/L2 (30 min.)	Ahmed Abo-Elhadid (Siemens EDA) for Didier Celi (ST Micro)
11:15 AM	Invited Speaker Presentations (60 min.)	
	Looking Ahead at Future Transistors and Memories	Sayeeef Salahuddin (UC Berkeley)
	Reflections on 50 Years with Semiconductors	Chenming Hu (UC Berkeley)
12:15 PM	Group Photo (10 min.)	Meeting room
12:25 PM	Lunch (65 min.)	Meeting room

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| 1:30 PM | BSIM-Bulk (15 min.) | Kaiman Chan (TI) |
| 1:45 PM | BSIM-SOI (15 min.) | Tanvir Morshed (GlobalFoundries) for Anupam Dutta (GlobalFoundries) |
| 2:00 PM | BSIM-CMG (15 min) | Colin Shaw (Silvaco) |
| 2:15 PM | BSIM-IMG (15 min.) | Tanvir Morshed (GlobalFoundries) |
| 2:30 PM | HiSIM2 (15 min.) | Colin Shaw (Silvaco) for Takeshi Naito (KIOXIA) |
| 2:45 PM | HiSIM_HV (15 min.) | Colin Shaw (Silvaco) for Takeshi Naito (KIOXIA) |
| 3:00 PM | HiSIM-SOI/SOTB (30 min.) | Samir Chaudhry (Tower Semiconductor) |
| 3:30 PM | Break (15 min.) | |

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| 3:45 PM | L-UTSOI (15 min.) | Harrison Lee (Samsung) |
| 4:00 PM | PSP (15 min.) | Gert-Jan Smit (NXP) |
| 4:15 PM | MVSG_CMC (15 min.) | Colin Shaw (Silvaco) |
| 4:30 PM | ASM-HEMT (15 min.) | Colin Shaw (Silvaco) |
| 4:45 PM | ASM-ESD (15 min.) | Michi Stockinger (NXP) |
| 5:00 PM | Model QA and Release (15 min.) | Shahriar Moinian (Broadcom) |
| 5:15 PM | Verilog-A Recommended Practices (15 min.) | Geoffrey Coram (Analog Devices) |
| 5:30 PM | Adjourn and Reception | il Fornaio (Italian Cuisine)
302 S. Market St., San Jose |

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| 9:00 AM | Agenda Review (5 min.) | Peter Lee (Micron) |
| 9:05 AM | Next Meeting Locations (10 min.) | Jushan Xie (Cadence) |
| 9:15 PM | ESD FET (15 min.) | Meng Miao (Broadcom) |
| 9:30 PM | OMI (15 min.) | Colin Shaw (Silvaco) |
| 9:45 PM | Random Telegraph Noise (15 min.) | Colin Shaw (Silvaco) |
| 10:00 AM | SiC (15 min.) | Colin Shaw (Silvaco) |
| 10:15 AM | Memristor (15 min.) | Colin Shaw (Silvaco) |
| 10:30 AM | Self-Heating (15 min.) | Richard Wachnik (IBM) |
| 10:45 AM | Diode_CMC (15 min.) | Klaus-Willi Pieper (Infineon) |
| 11:00 AM | Break (15 min.) | |

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| 11:15 AM | One-page Group Summaries (30 min.) | All work group chairs |
| 11:45 AM | Next Meeting Planning (10 min.) | Peter Lee (Micron) |
| 11:55 AM | Action Items Review (5 min.) | Tanvir Morshed (GlobalFoundries) |

12:00 PM Adjourn

Officers/Advisors

Position	Name
Chairman	Peter M. Lee (Micron) (pmlee.si2@icloud.com)
Vice-Chairman	Jushan Xie (Cadence) (jushan@cadence.com)
Secretary	Tanvir Morshed (GlobalFoundries) (Tanvir.Morshed@globalfoundries.com)
Treasurer	Takeshi Naito (KIOXIA) (takeshi.naito@kioxia.com)
Technical Advisor: Verilog-A Code Quality Improvement	Geoffrey Coram (Analog Devices) (geoffrey.coram@analog.com)
Technical Advisor: Model QA and Release	Shahriar Moinian (Broadcom) (shahriar.moinian@Broadcom.com)

Term for the four officers: 2024 - 2025
Term for the Technical Advisors: no fixed term

Active Work Groups in the CMC

Work Group Names (CMOS Devices)	Chair
BSIM-Bulk	Kaiman Chan (Texas Instruments) (kaiman.chan@ti.com)
BSIM-CMG	Colin Shaw (Silvaco) (colin.shaw@silvaco.com)
BSIM-SOI	Anupam Dutta (GlobalFoundries) (anupam.dutta@globalfoundries.com)
BSIM-IMG	Tanvir Morshed (GlobalFoundries) (tanvir.morshed@globalfoundries.com)
HiSIM2	Takeshi Naito (KIOXIA) (takeshi.naito@kioxia.com)
HiSIM_HV	Takeshi Naito (KIOXIA) (takeshi.naito@kioxia.com)
HiSIM_SOI	Samir Chaudhry (Tower Semiconductor) (Samir.Chaudhry@towersemi.com)
HiSIM_SOTB	(vacant)
PSP	Andries Scholten (NXP) (andries.scholten@nxp.com) and Gert-Jan Smit (NXP) (gert-jan.smit@nxp.com)
L-UTSOI	Harrison Lee (Samsung) (lhw114@Samsung.com)

Active Work Groups in the CMC (continued)

Work Group Name (Bipolar Devices)	Chair
HICUM/L0 & HICUM/L2	Didier Celi (ST Micro) (didier.celi@st.com)
Mextram	Wei Chen (Texas Instruments) (wei.chen@ti.com)

Work Group Name (Other Devices)	Chair
ASM-HEMT, MVSG_CMC	Colin Shaw (Silvaco) (colin.shaw@silvaco.com)
ASM-ESD Diode Model	Michi Stockinger (NXP) (m.stockinger@nxp.com)
MOSVAR, Resistor Models	Geoffrey Coram (Analog Devices) (geoffrey.coram@analog.com)
SiC (not yet a standard)	Colin Shaw (Silvaco) (colin.shaw@silvaco.com)
ESD FET Model (not yet a standard)	Meng Miao (Broadcom) (meng.miao@broadcom.com)

Active Work Groups in the CMC (continued)

Work Group Name (Other Standards)	Chair
OMI API	Colin Shaw (Silvaco) (colin.shaw@silvaco.com)
Reliability Model (not yet a standard)	Klaus-Willi Pieper (Infineon) (klaus-willi.pieper@infineon.com)
Random Telegraph Noise (not yet a standard)	Colin Shaw (Silvaco) (colin.shaw@silvaco.com)

Work Group Name (Operational)	Chair
Model QA and Release	Shahriar Moinian (Broadcom) (shahriar.moinian@broadcom.com)
Verilog-A Recommended Practices	Geoffrey Coram (Analog Devices) (Geoffrey.coram@analog.com)

Si2 Staff

Area of Responsibility	Lead
CEO	Robert Aslett (robert_aslett@si2.org)
VP Technology: Si2/CMC Management Synchronization	Leigh Anne Clevenger (leighanne.clevenger@si2.org)
Executive Technical Assistant: Model and application release management and QA	Marc Rose (marc.rose@si2.org)
Member Relations: Member onboarding, CMC events, member access control	Alan LaRotonda (alan.larotonda@si2.org)

