

Agenda for the 2024Q1 CMC Meeting

MINATEC: Grenoble, France

Thursday, March 21, 2024

Version 0

MEMBERS AND GUEST COMPANIES ONLY

- | | | |
|----------|---------------------------------|------------------------|
| 9:00 AM | Introductions (5 min.) | Peter Lee (Micron) |
| 9:05 AM | Agenda Review (5 min.) | Peter Lee (Micron) |
| 9:10 AM | Update from Si2 (30 min.) | Rob Aslett (Si2) |
| 9:40 AM | Treasurer's Report (5 min.) | Takeshi Naito (KIOXIA) |
| 9:45 AM | CMC Operational Items (15 min.) | Peter Lee (Micron) |
| 10:00 AM | Break (15 min.) | |

Agenda for the 2024Q1 CMC Meeting

MINATEC: Grenoble, France

Thursday, March 21, 2024

ALL (includes Model Developers)

- | | | |
|----------|------------------------------|--------------------------------------|
| 10:15 AM | EDA Vendor Reports (30 min.) | EDA Vendors |
| 10:45 AM | HiSIM2 (15 min.) | Takeshi Naito (KIOXIA) |
| 11:00 AM | HiSIM_HV (15 min.) | Takeshi Naito (KIOXIA) (acting) |
| 11:15 AM | HiSIM-SOI/SOTB (30 min.) | Samir Chaudhry (Tower Semiconductor) |
| 11:45 AM | BSIM-Bulk (15 min.) | Kaiman Chan (TI) |
| 12:00 PM | Group Photo (5 min.) | |
| 12:05 PM | Lunch (55 min.) | |

Agenda for the 2024Q1 CMC Meeting

MINATEC: Grenoble, France

Thursday, March 21, 2024

ALL (includes Model Developers)

| | | |
|---------|--------------------|----------------------------------|
| 1:00 PM | BSIM-CMG (15 min) | Colin Shaw (Silvaco) |
| 1:15 PM | BSIM-SOI (15 min.) | Anupam Dutta (GlobalFoundries) |
| 1:30 PM | BSIM-IMG (15 min.) | Tanvir Morshed (GlobalFoundries) |
| 1:45 PM | PSP (15 min.) | Gert-Jan Smit (NXP) |
| 2:00 PM | L-UTSOI (15 min.) | Harrison Lee (Samsung) |
| 2:15 PM | MVSG_CMC (15 min.) | Colin Shaw (Silvaco) |
| 2:30 PM | ASM-HEMT (15 min.) | Colin Shaw (Silvaco) |
| 2:45 PM | ASM-ESD (15 min.) | Michi Stockinger (NXP) |
| 3:00 PM | ESD FET (15 min.) | Meng Miao (Broadcom) |
| 3:15 PM | Break (15 min.) | |

Agenda for the 2024Q1 CMC Meeting

MINATEC: Grenoble, France

Thursday, March 21, 2024

ALL (includes Model Developers)

3:30 PM Model QA and Release (15 min.)

Shahriar Moinian (Broadcom)

3:45 PM Verilog-A Recommended Practices (15 min.)

Geoffrey Coram (Analog Devices)

4:00 PM Diode_CMC (15 min.)

Klaus-Willi Pieper (Infineon)

4:15 PM Reliability (15 min.)

Klaus-Willi Pieper (Infineon)

4:30 PM Guest Speaker (45 min.)

5:15 PM **Adjourn**

Agenda for the 2024Q1 CMC Meeting

MINATEC: Grenoble, France

Friday, March 22, 2024

ALL (includes Model Developers)

| | | |
|----------|--|-----------------------|
| 9:00 AM | Agenda Review (5 min.) | Peter Lee (Micron) |
| 9:05 AM | Next Meeting Locations (10 min.) | Jushan Xie (Cadence) |
| 9:15 AM | OMI (15 min.) | Colin Shaw (Silvaco) |
| 9:30 AM | Random Telegraph Noise (15 min.) | Colin Shaw (Silvaco) |
| 9:45 AM | SiC (15 min.) | Colin Shaw (Silvaco) |
| 10:00 AM | Memristor (15 min.) | Colin Shaw (Silvaco) |
| 10:15 AM | Self-Heating Model Standardization (15 min.) | Richard Wachnik (IBM) |
| 10:30 AM | Break (15 min.) | |

Agenda for the 2024Q1 CMC Meeting

MINATEC: Grenoble, France

Friday, March 22, 2024

ALL (includes Model Developers)

10:45 AM One-page Group Summaries (30 min.)

11:15 AM CMC Annual Survey: Leadership Response (15 min.)

11:30 AM Next Meeting Planning (10 min.)

11:40 AM Action Items Review (10 min.)

11:50 AM **Adjourn**

All work group chairs

CMC Officers, Si2

Peter Lee (Micron)

Tanvir Morshed
(GlobalFoundries)



Officers/Advisors

| Position | Name |
|---|--|
| Chairman | Peter M. Lee (Micron) (pmlee.si2@icloud.com) |
| Vice-Chairman | Jushan Xie (Cadence) (jushan@cadence.com) |
| Secretary | Tanvir Morshed (GlobalFoundries) (Tanvir.Morshed@globalfoundries.com) |
| Treasurer | Takeshi Naito (KIOXIA) (takeshi.naito@kioxia.com) |
| Technical Advisor: Verilog-A Code Quality Improvement | Geoffrey Coram (Analog Devices) (geoffrey.coram@analog.com) |
| Technical Advisor: Model QA and Release | Shahriar Moinian (Broadcom) (shahriar.moinian@Broadcom.com) |

Term for the four officers: 2024 - 2025

Term for the Technical Advisors: no limits

Active Work Groups in the CMC

| Work Group Names (CMOS Devices) | Chair |
|---------------------------------|---|
| BSIM-Bulk | Kaiman Chan (Texas Instruments) (kaiman.chan@ti.com) |
| BSIM-CMG | Colin Shaw (Silvaco) (colin.shaw@silvaco.com) |
| BSIM-SOI | Anupam Dutta (GlobalFoundries) (anupam.dutta@globalfoundries.com) |
| BSIM-IMG | Tanvir Morshed (GlobalFoundries) (tanvir.morshed@globalfoundries.com) |
| HiSIM2 | Takeshi Naito (KIOXIA) (takeshi.naito@kioxia.com) |
| HiSIM_HV | (vacant) |
| HiSIM_SOI, HiSIM_SOTB | Samir Chaudhry (Tower Semiconductor) (Samir.Chaudhry@towersemi.com) |
| PSP | Andries Scholten (NXP) (andries.scholten@nxp.com) and Gert-Jan Smit (NXP) (gert-jan.smit@nxp.com) |
| L-UTSOI | Harrison Lee (Samsung) (lhw114@Samsung.com) |

Active Work Groups in the CMC (continued)

| Work Group Name (Bipolar Devices) | Chair |
|-----------------------------------|---|
| HICUM/L0 & HICUM/L2 | Didier Celi (ST Micro) (didier.celi@st.com) |
| Mextram | Wei Chen (Texas Instruments) (wei.chen@ti.com) |

| Work Group Name (Other Devices) | Chair |
|--------------------------------------|--|
| ASM-HEMT, MVSG_CMC | Colin Shaw (Silvaco) (colin.shaw@silvaco.com) |
| ASM-ESD Diode Model | Michi Stockinger (NXP) (m.stockinger@nxp.com) |
| MOSVAR, Resistor Models | Geoffrey Coram (Analog Devices) (geoffrey.coram@analog.com) |
| SiC (not yet a standard) | Colin Shaw (Silvaco) (colin.shaw@silvaco.com) |
| ESD FET Model (not yet a standard) | Meng Miao (Broadcom) (meng.miao@broadcom.com) |
| Memristor Model (not yet a standard) | Colin Shaw (Silvaco) (colin.shaw@silvaco.com) |

Active Work Groups in the CMC (continued)

| Work Group Name (Other Standards) | Chair |
|---|--|
| OMI API | Colin Shaw (Silvaco) (colin.shaw@silvaco.com) |
| Reliability Model (not yet a standard) | Klaus-Willi Pieper (Infineon) (klaus-willi.pieper@infineon.com) |
| Random Telegraph Noise (not yet a standard) | Colin Shaw (Silvaco) (colin.shaw@silvaco.com) |
| Self-Heating Model (not yet a standard) | Richard Wachnik (IBM) |

| Work Group Name (Operational) | Chair |
|---------------------------------|--|
| Model QA and Release | Shahriar Moinian (Broadcom) (shahriar.moinian@broadcom.com) |
| Verilog-A Recommended Practices | Geoffrey Coram (Analog Devices) (Geoffrey.coram@analog.com) |

Si2 Staff

| Area of Responsibility | Lead |
|--|---|
| CEO | Robert Aslett |
| VP Technology: Si2/CMC Management Synchronization | Leigh Anne Clevenger (leighanne.clevenger@si2.org) |
| Executive Technical Assistant: Model and application release management and QA | Marc Rose (marc.rose@si2.org) |
| Member Relations: Member onboarding, CMC events, member access control | Alan LaRotonda (alan.larotonda@si2.org) |