

# Agenda for the 2023Q4 CMC Meeting Texas Instruments, Santa Clara, California

Thursday, December 7, 2023

*Version 1*

## **MEMBERS AND GUEST COMPANIES ONLY**

- |         |   |  |
|---------|---|--|
| 9:00 AM | Agenda Review and Introductions (10 min.) | Peter Lee (Micron)                                 |
| 9:10 AM | Update from Si2 (30 min.)                 | Leigh Anne Clevenger (Si2) for Robert Aslett (Si2) |
| 9:40 AM | Treasurer's Report (5 min.)               | Peter Lee (Micron) for Takeshi Naito (KIOXIA)      |
| 9:45 AM | CMC Operational Items (5 min.)            | Peter Lee (Micron)                                 |
| 9:50 AM | Break (10 min.)                           |  |

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### ALL (includes Model Developers)

10:00 AM	EDA Vendor Reports (30 min.)	EDA Vendors
10:30 AM	HICUM/L0 (15 min.)	Didier Celi (ST Micro)
10:45 AM	HICUM/L2 (15 min.)	Didier Celi (ST Micro)
11:00 AM	Mextram (15 min.)	Wei Chen (TI)
11:15 AM	BSIM-Bulk (15 min.)	Kaiman Chan (TI)
11:30 AM	BSIM-SOI (15 min.)	Jung-Suk Goo (GlobalFoundries) for Anupam Dutta (GlobalFoundries)
11:45 AM	BSIM-CMG (15 min)	Colin Shaw (Silvaco)
12:00 PM	Group Photo (10 min.)	Meeting room
12:10 PM	Lunch (50 min.)	Meeting room

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1:00 PM	BSIM-IMG (15 min.)	Jung-Suk Goo (GlobalFoundries) for Tanvir Morshed (GlobalFoundries)
1:15 PM	HiSIM2 (15 min.)	Peter Lee (Micron) for Takeshi Naito (KIOXIA)
1:30 PM	HiSIM_HV (15 min.)	Peter Lee (Micron) for Takeshi Naito (KIOXIA) (acting)
1:45 PM	HiSIM-SOI/SOTB (30 min.)	Samir Chaudhry (Tower Semiconductor)
2:15 PM	PSP (15 min.)	Gert-Jan Smit (NXP)
2:30 PM	L-UTSOI (15 min.)	Harrison Lee (Samsung)
2:45 PM	Break (15 min.)	

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|---------|---|--|
| 3:00 PM | MVSG_CMC (15 min.)                        | Colin Shaw (Silvaco)                               |
| 3:15 PM | ASM-HEMT (15 min.)                        | Colin Shaw (Silvaco)                               |
| 3:30 PM | ASM-ESD (15 min.)                         | Michi Stockinger (NXP)                             |
| 3:45 PM | Model QA and Release (15 min.)            | Peter Lee (Micron) for Shahriar Moinian (Broadcom) |
| 4:00 PM | Verilog-A Recommended Practices (15 min.) | Geoffrey Coram (Analog Devices)                    |
| 4:15 PM | ESD FET (15 min.)                         | Meng Miao (GlobalFoundries)                        |
| 4:30 PM | OMI (15 min.)                             | Colin Shaw (Silvaco)                               |
| 4:45 PM | Random Telegraph Noise (15 min.)          | Colin Shaw (Silvaco)                               |
| 5:00 PM | Adjourn and Reception                     |  |

# Agenda for the 2023Q4 CMC Meeting

## Texas Instruments, Santa Clara, California

Friday, December 8, 2023

### ALL (includes Model Developers)

9:00 AM	Agenda Review (5 min.)	Peter Lee (Micron)
9:05 AM	Next Meeting Locations (10 min.)	Jushan Xie (Cadence)
9:15 AM	SiC (15 min.)	Colin Shaw (Silvaco)
9:30 AM	Memristor (15 min.)	Colin Shaw (Silvaco)
9:45 AM	Self-Heating (15 min.)	Richard Wachnik (IBM)
10:00 AM	Break (15 min.)	

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## Texas Instruments, Santa Clara, California

Friday, December 8, 2023

### ALL (includes Model Developers)

- |          |  |   |
|----------|--|---|
| 10:15 AM | One-page Group Summaries (30 min.)     | All work group chairs                                   |
| 10:45 AM | CMC Annual Survey Results (15 min.)    | Leigh Anne Clevenger (Si2)                              |
| 11:00 AM | Call out for new Work Groups (10 min.) | Peter Lee (Micron)                                      |
| 11:10 AM | Next Meeting Planning (10 min.)        | Peter Lee (Micron)                                      |
| 11:20 AM | Action Items Review (10 min.)          | Peter Lee (Micron) for Tanvir Morshed (GlobalFoundries) |
| 11:30 PM | Adjourn                                |   |

## Officers/Advisors

Position	Name
Chairman	Peter M. Lee (Micron) ( <a href="mailto:pmlee.si2@icloud.com">pmlee.si2@icloud.com</a> )
Vice-Chairman	Jushan Xie (Cadence) ( <a href="mailto:jushan@cadence.com">jushan@cadence.com</a> )
Secretary	Tanvir Morshed (GlobalFoundries) ( <a href="mailto:Tanvir.Morshed@globalfoundries.com">Tanvir.Morshed@globalfoundries.com</a> )
Treasurer	Takeshi Naito (KIOXIA) ( <a href="mailto:takeshi.naito@kioxia.com">takeshi.naito@kioxia.com</a> )
Technical Advisor: Verilog-A Code Quality Improvement	Geoffrey Coram (Analog Devices) ( <a href="mailto:geoffrey.coram@analog.com">geoffrey.coram@analog.com</a> )
Technical Advisor: Model QA and Release	Shahriar Moinian (Broadcom) ( <a href="mailto:shahriar.moinian@Broadcom.com">shahriar.moinian@Broadcom.com</a> )

Term for the four officers: 2024 - 2025  
 Term for the Technical Advisors: not fixed

## Active Work Groups in the CMC

Work Group Names (CMOS Devices)	Chair
BSIM-Bulk	Kaiman Chan (Texas Instruments) ( <a href="mailto:kaiman.chan@ti.com">kaiman.chan@ti.com</a> )
BSIM-CMG	Colin Shaw (Silvaco) ( <a href="mailto:colin.shaw@silvaco.com">colin.shaw@silvaco.com</a> )
BSIM-SOI	Anupam Dutta (GlobalFoundries) ( <a href="mailto:anupam.dutta@globalfoundries.com">anupam.dutta@globalfoundries.com</a> )
BSIM-IMG	Tanvir Morshed (GlobalFoundries) ( <a href="mailto:tanvir.morshed@globalfoundries.com">tanvir.morshed@globalfoundries.com</a> )
HiSIM2	Takeshi Naito (KIOXIA) ( <a href="mailto:takeshi.naito@kioxia.com">takeshi.naito@kioxia.com</a> )
HiSIM_HV	(vacant)
HiSIM_SOI, HiSIM_SOTB	Samir Chaudhry (Tower Semiconductor) ( <a href="mailto:Samir.Chaudhry@towersemi.com">Samir.Chaudhry@towersemi.com</a> )
PSP	Andries Scholten (NXP) ( <a href="mailto:andries.scholten@nxp.com">andries.scholten@nxp.com</a> ) and Gert-Jan Smit (NXP) ( <a href="mailto:gert-jan.smit@nxp.com">gert-jan.smit@nxp.com</a> )
L-UTSOI	Harrison Lee (Samsung) ( <a href="mailto:lhw114@Samsung.com">lhw114@Samsung.com</a> )



## Active Work Groups in the CMC (continued)

Work Group Name (Bipolar Devices)	Chair
HICUM/L0 & HICUM/L2	Didier Celi (ST Micro) ( <a href="mailto:didier.celi@st.com">didier.celi@st.com</a> )
Mextram	Wei Chen (Texas Instruments) ( <a href="mailto:wei.chen@ti.com">wei.chen@ti.com</a> )

Work Group Name (Other Devices)	Chair
ASM-HEMT, MVSG_CMC	Colin Shaw (Silvaco) ( <a href="mailto:colin.shaw@silvaco.com">colin.shaw@silvaco.com</a> )
ASM-ESD Diode Model	Michi Stockinger (NXP) ( <a href="mailto:m.stockinger@nxp.com">m.stockinger@nxp.com</a> )
MOSVAR, Resistor Models	Geoffrey Coram (Analog Devices) ( <a href="mailto:geoffrey.coram@analog.com">geoffrey.coram@analog.com</a> )
SiC (not yet a standard)	Colin Shaw (Silvaco) ( <a href="mailto:colin.shaw@silvaco.com">colin.shaw@silvaco.com</a> )

## Active Work Groups in the CMC (continued)

Work Group Name (Other Standards)	Chair
OMI API	Colin Shaw (Silvaco) ( <a href="mailto:colin.shaw@silvaco.com">colin.shaw@silvaco.com</a> )
Reliability Model (not yet a standard)	Klaus-Willi Pieper (Infineon) ( <a href="mailto:klaus-willi.pieper@infineon.com">klaus-willi.pieper@infineon.com</a> )
Random Telegraph Noise (not yet a standard)	Colin Shaw (Silvaco) ( <a href="mailto:colin.shaw@silvaco.com">colin.shaw@silvaco.com</a> )

Work Group Name (Operational)	Chair
Model QA and Release	Shahriar Moinian (Broadcom) ( <a href="mailto:shahriar.moinian@broadcom.com">shahriar.moinian@broadcom.com</a> )
Verilog-A Recommended Practices	Geoffrey Coram (Analog Devices) ( <a href="mailto:Geoffrey.coram@analog.com">Geoffrey.coram@analog.com</a> )

## Si2 Staff

Area of Responsibility	Lead
CEO	Robert Aslett
VP Technology: Si2/CMC Management Synchronization	Leigh Anne Clevenger ( <a href="mailto:leighanne.clevenger@si2.org">leighanne.clevenger@si2.org</a> )
Executive Technical Assistant: Model and application release management and QA	Marc Rose ( <a href="mailto:marc.rose@si2.org">marc.rose@si2.org</a> )
Member Relations: Member onboarding, CMC events, member access control	Alan LaRotonda ( <a href="mailto:alan.larotonda@si2.org">alan.larotonda@si2.org</a> )

