

2023Q1 CMC Meeting Work Group Virtual Zoom Meeting Schedules

Wednesday, March 15 – Thursday, March 16, 2023

Version 6

Wednesday, March 15

8:55 AM	Agenda Review and IP Notice	Peter Lee (Micron)
9:00 AM	ASM-HEMT (15 min.)	Colin Shaw (Silvaco)
9:15 AM	MVSG_CMC (15 min.)	Colin Shaw (Silvaco)
9:30 AM	Model QA and Release (15 min.)	Shahriar Moinian (Broadcom)
9:45 AM	HiSIM2 (15 min.)	Takeshi Naito (KIOXIA)
10:00 AM	HiSIM_HV (15 min.)	Hiroataka Takatsuka (United Semiconductor Japan)
10:15 AM	HiSIM-SOI/SOTB (30 min.)	Samir Chaudhry (Tower Semiconductor)
10:45 AM	BSIM-BULK (15 min.)	Kaiman Chan (TI)
11:00 AM	BSIM-CMG (non-GAA) (15 min.)	Colin Shaw (Silvaco)
11:15 AM	BSIM-CMG (GAA) (15 min.)	Colin Shaw (Silvaco)
11:30 AM	BSIM-IMG (15 min.)	Tanvir Morshed (GlobalFoundries)
11:45 AM	BSIM-SOI (15 min.)	Anupam Dutta (GlobalFoundries)

12:00 PM Adjourn

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Wednesday, March 15 – Thursday, March 16, 2023

Thursday, March 16

8:55 AM Agenda Review and IP Notice	Peter Lee (Micron)
9:00 AM ASM-ESD (15 min.)	Meng Miao (GlobalFoundries)
9:15 AM L-UTSOI (15 min.)	Harrison Lee (Samsung)
9:30 AM PSP (15 min.)	Gert-Jan Smit (NXP)
9:45 AM RTN (15 min.)	Colin Shaw (Silvaco)
10:00 AM SiC (15 min.)	Colin Shaw (Silvaco)
10:15 AM OMI (15 min.)	Colin Shaw (Silvaco)
10:30 AM Reliability Model (15 min.)	Klaus-Willi Pieper (Infineon)
10:45 AM Verilog-A Recommended Practices (15 min.)	Geoffrey Coram (ADI)
11:00 AM Adjourn	

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2023Q1 CMC Meeting Member/Guest Virtual Zoom Meeting Schedule

Friday, March 17, 2023

MEMBERS AND GUEST COMPANIES ONLY (9:00-10:05)

9:00 AM Agenda Review and Introductions (10 min.)	Peter Lee (Micron)
9:10 AM Update from Si2 (30 min.)	John Ellis (Si2)
9:40 AM Treasurer's Report (5 min.)	Takeshi Naito (KIOXIA)
9:45 AM CMC Operational Items (10 min.)	Peter Lee (Micron)
9:55 AM R3_CMC Updates (10 min.)	Geoffrey Coram (Analog Devices)

10:05 AM Break (5 min.)

(Switch to the All Hands Zoom meeting link)

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2023Q1 CMC Meeting All Hands Virtual Zoom Meeting Schedule

Friday, March 17, 2023

ALL (includes Model Developers) (10:10-11:55)

10:10 AM Agenda Review (5 min.)	Peter Lee (Micron)
10:15 AM Future Meeting Locations (15 min.)	Jushan Xie (Cadence)
10:30 AM MINR Enhancement (15 min.)	Geoffrey Coram (Analog Devices)
10:45 AM Memristor Model Standardization (20 min.)	Colin Shaw (Silvaco)
11:05 AM New ESD Work Group Proposal (10 min.)	Meng Miao (GlobalFoundries)
11:15 AM mult_i/mult_q/mult_fn (15 min.)	Geoffrey Coram (Analog Devices)
11:30 AM CMC Annual Survey: Officers Response (10 min.)	Peter Lee (Micron)
One-page Group Summaries (post online)	Work group chairs
11:40 AM Next Q2 Meeting Planning (5 min.)	Peter Lee (Micron)
11:45 AM Action Items Review (10 min.)	Tanvir Morshed (GlobalFoundries)
11:55 AM Group Photo and Adjourn	

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Officers/Advisors

Chairman	Peter M. Lee (Micron) (pmlee.si2@icloud.com)
Vice-Chairman	Jushan Xie (Cadence) (jushan@cadence.com)
Secretary	Tanvir Morshed (GlobalFoundries) (Tanvir.Morshed@globalfoundries.com)
Treasurer	Takeshi Naito (KIOXIA) (takeshi.naito@kioxia.com)
Technical Advisor: Verilog-A Code Quality Improvement	Geoffrey Coram (Analog Devices) (geoffrey.coram@analog.com)
Technical Advisor: Model QA and Release	Shahriar Moinian (Broadcom) (shahriar.moinian@Broadcom.com)

Officer Term: 2022 - 2023

Active Work Groups in the CMC

Work Group Names (CMOS Devices)	Chair
BSIM-Bulk	Kaiman Chan (Texas Instruments) (kaiman.chan@ti.com)
BSIM-CMG	Colin Shaw (Silvaco) (colin.shaw@silvaco.com)
BSIM-SOI	Anupam Dutta (GlobalFoundries) (anupam.dutta@globalfoundries.com)
BSIM-IMG	Tanvir Morshed (GlobalFoundries) (tanvir.morshed@globalfoundries.com)
HiSIM2	Takeshi Naito (KIOXIA) (takeshi.naito@kioxia.com)
HiSIM_HV	Hiroataka Takatsuka (United Semiconductor Japan) (takatuka@usjpc.com)
HiSIM_SOI, HiSIM_SOTB	Samir Chaudhry (Tower Semiconductor) (Samir.Chaudhry@towersemi.com)
PSP	Andries Scholten (NXP) (andries.scholten@nxp.com) and Gert-Jan Smit (NXP) (gert-jan.smit@nxp.com)
L-UTSOI	Harrison Lee (Samsung) (lhw114@Samsung.com)

Active Work Groups in the CMC (continued)

Work Group Name (Bipolar Devices)	Chair
HICUM/L0 & HICUM/L2	Didier Celi (ST Micro) (didier.celi@st.com)
Mextram	Wei Chen (Texas Instruments) (wei.chen@ti.com)

Work Group Name (Other Devices)	Chair
ASM-HEMT, MVSG_CMC	Colin Shaw (Silvaco) (colin.shaw@silvaco.com)
ASM-ESD Diode Model	Meng Miao (GlobalFoundries) (meng.miao@globalfoundries.com)
MOSVAR, Resistor Models	Geoffrey Coram (Analog Devices) (geoffrey.coram@analog.com)
SiC (not yet a standard)	Colin Shaw (Silvaco) (colin.shaw@silvaco.com)

Active Work Groups in the CMC (continued)

Work Group Name (Other Standards)	Chair
OMI API	Colin Shaw (Silvaco) (colin.shaw@silvaco.com)
Reliability Model (not yet a standard)	Klaus-Willi Pieper (Infineon) (klaus-willi.pieper@infineon.com)
Random Telegraph Noise (not yet a standard)	Colin Shaw (Silvaco) (colin.shaw@silvaco.com)

Work Group Name (Operational)	Chair
Model QA and Release	Shahriar Moinian (Broadcom) (shahriar.moinian@broadcom.com)
Verilog-A Recommended Practices	Geoffrey Coram (Analog Devices) (Geoffrey.coram@analog.com)

Si2 Staff

Area of Responsibility	Lead
CEO and CMC Liaison	John Ellis (john.ellis@si2.org)
Meeting Arrangements	Kat Grace (kat.grace@si2.org)
Web Site/IT Development and Support	Phillip Isenhardt (phillip.isenhardt@si2.org)

