

Agenda for the 2019Q3 CMC Meeting NXP Semiconductors, Austin, Texas

Thursday, September 19, 2019

Version 3

MEMBERS AND GUEST COMPANIES ONLY

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|----------|-------------------------------------------|---------------------------------------------------------|
| 8:30 AM | Agenda Review and Introductions (15 min.) | Peter Lee (Micron) |
| 8:45 AM | Update from Si2 (30 min.) | John Ellis (Si2) |
| 9:15 AM | Treasurer's Report (5 min.) | Takeshi Naito (Toshiba Memory) |
| 9:20 AM | CMC Operational Items (10 min.) | Peter Lee (Micron) |
| 9:30 AM | HICUM/L0 Phase IV Report (20 min.) | Ahmed Ramadan (Mentor Graphics) for
Didier Celi (ST) |
| 9:50 AM | L-UTSOI Phase III Report (20 min.) | Harrison Lee (Samsung) |
| 10:10 AM | R2_CMC/R3_CMC model update (25 min.) | Geoffrey Coram (Analog Devices) |
| 10:35 AM | Break (15 min.) | |

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ALL (includes Model Developers)

10:50 AM	EDA Vendor Reports (30 min.)	EDA Vendors
11:20 AM	BSIM-Bulk (30 min.)	Kaiman Chan (TI)
11:50 AM	Group Photo (10 min.)	
12:00 PM	Lunch (60 min.)	

Green: Not yet confirmed

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1:00 PM	BSIM-CMG (20 min.)	Richard Williams (IBM)
1:20 PM	BSIM-SOI (10 min.)	Glenn Workman (GLOBALFOUNDRIES) for Anupam Dutta (GLOBALFOUNDRIES)
1:30 PM	BSIM-IMG (15 min.)	Glenn Workman (GLOBALFOUNDRIES) for Tanvir Morshed (GLOBALFOUNDRIES)
1:45 PM	HiSIM2 (30 min.)	Takeshi Naito (Toshiba Memory)
2:15 PM	HiSIM_HV (30 min.)	Takeshi Naito (Toshiba Memory)
2:45 PM	HiSIM_SOI (15 min.)	Marek Mierzwinski (Keysight)
3:00 PM	HiSIM_SOTB (20 min.)	Marek Mierzwinski (Keysight)
3:20 PM	Break (15 min.)	

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|---------|-------------------------------------------------------|-----------------------------------------------|
| 3:35 PM | PSP (30 min.) | Gert-Jan Smit (NXP for Andries Scholten (NXP) |
| 4:05 PM | Model QA and Release (15 min.) | Shahriar Moinian (Broadcom) |
| 4:20 PM | Verilog-A Recommended Practices (45 min.) | Geoffrey Coram (Analog Devices) |
| 5:05 PM | Adjourn and Reception (6:30PM – 9:30PM Dinner cruise) | |

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MEMBERS AND GUEST COMPANIES ONLY

8:30 AM CMC Administration (5 min.) Peter Lee (Micron)

ALL (includes Model Developers)

8:35 AM Agenda Review (5 min.) Peter Lee (Micron)

8:40 AM Next Meeting Locations (10 min.) Jushan Xie (Cadence)

8:50 AM ASM-HEMT GaN HEMT Model (15 min.) Colin Shaw (Silvaco)

9:05 AM MVSG_CMC GaN HEMT Model (15 min.) Colin Shaw (Silvaco)

9:20 AM HICUM\L2 (10 min.) Colin Shaw (Silvaco)

9:30 AM Advanced Multi-Gate Models (15 min.) Richard Williams (IBM)

9:45 AM ESD (10 min.) Glenn Workman (GLOBALFOUNDRIES) for You Li (GLOBALFOUNDRIES)

9:55 AM Break (15 min.)

Green: Not yet confirmed

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10:10 AM	OMI (15 min.)	Colin Shaw (Silvaco)
10:25 AM	Reliability Model Standardization (30 min.)	Klaus-Willi Pieper (Infineon)
10:55 AM	GMIN (10 min.)	Ahmed Ramadan (Mentor Graphics)
11:05 AM	Random Telegraph Noise (15 min.)	Y.C. Liang (TSMC) for Min-Chie Jeng (TSMC)
11:20 AM	Break (15 min.)	

Green: Not yet confirmed

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ALL (includes Model Developers)

- 11:35 AM One-page Group Summaries (30 min.)
- 12:05 PM Next Meeting Planning (10 min.)
- 12:15 PM Action Items Review (10 min.)
- 12:25 PM **Adjourn**
- 12:25 PM **Lunch (65 min.)**

Special Program

- 1:30 PM Si2 SharePoint Tutorial (30 min.) Phillip Isenhart (Si2)
- 2:00 PM OMI Tutorial (90 min.) Colin Shaw (Silvaco)

Officers/Advisors

Chairman	Peter M. Lee (Micron)
Vice-Chairman	Jushan Xie (Cadence)
Secretary	Richard Williams (IBM)
Treasurer	Takeshi Naito (Toshiba Memory)
Technical Advisor: Verilog-A Code Quality Improvement	Geoffrey Coram (Analog Devices)
Technical Advisor: Model QA and Release	Shahriar Moinian (Broadcom)

Active Work Groups in the CMC

Work Group Names (CMOS Devices)	Chair
BSIM-Bulk	Kaiman Chan (Texas Instruments)
BSIM-CMG, Advanced Multigate Model	Richard Williams (IBM)
BSIM-SOI	Anupam Dutta (GLOBALFOUNDRIES)
BSIM-IMG	Tanvir Morshed (GLOBALFOUNDRIES)
HiSIM2, HiSIM_HV	Takeshi Naito (Toshiba Memory)
HiSIM_SOI, HiSIM_SOTB	Marek Mierzwinski (Keysight)
PSP	Andries Scholten (NXP)
L-UTSOI	Harrison Lee (Samsung)

Active Work Groups in the CMC (continued)

Work Group Name (Bipolar Devices)	Chair
HICUM	Didier Celi (ST Micro)
Mextram	Wei Chen (Texas Instruments)

Work Group Name (Other Devices)	Chair
ASM-HEMT, MVSG_CMC	Colin Shaw (Silvaco)
MOSVAR	Geoffrey Coram (Analog Devices)
ESD Compact Models	You Li (GLOBALFOUNDRIES)

Active Work Groups in the CMC (continued)

Work Group Name (Other Standards)	Chair
Model QA and Release	Shahriar Moinian (Broadcom)
OMI API	Colin Shaw (Silvaco)
Reliability Model Standardization (not yet a standard)	Klaus-Willi Pieper (Infineon)
Verilog-A Recommended Practices	Geoffrey Coram (Analog Devices)
Operating Point Variable Names and Definitions	Samuel Mertens (Cadence)
Random Telegraph Noise (not yet a standard)	Min-Chie Jeng (TSMC)

Si2 Staff

Area of Responsibility	Lead
CEO and CMC Liaison	John Ellis
Meeting Arrangements	Sandy Lawrence
Web Site/IT Development and Support	Phillip Isenhardt

